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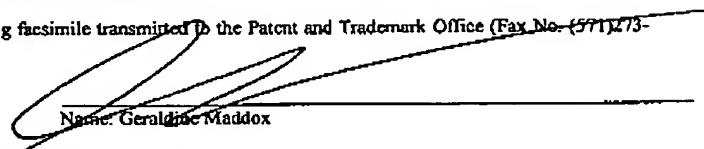
DEC 16 2005 Docket No. 740756-2664

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
Tatsuya HONDA et al.) Group Art Unit: 2881
Serial No. 10/700,719) Confirmation No.: 6004
Filed: November 5, 2003) Examiner: Not Yet Assigned
For: EVALUATION METHOD OF)
SEMICONDUCTOR DEVICE,)
MANUFACTURING METHOD OF THE)
SEMICONDUCTOR DEVICE)

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I hereby certify that this correspondence is being facsimile transmitted to the Patent and Trademark Office (Fax No. (571)273-8300 on December 16, 2005.


Name: Geraldine Maddox

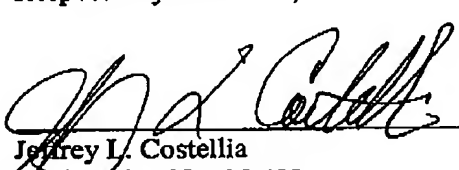
STATUS INQUIRY

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

Please advise, in writing, as to the current status of the above-identified application.

Respectfully submitted,


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Dated: December 16, 2005

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